

CUSTOMER NO.: 24498  
Serial No.: 10/791,343  
Office Action dated: 09/20/06  
Response dated: 03/20/07

PATENT  
PA030009

Listing of the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (currently amended) Arrangement comprising  
a microprocessor,  
a demagnetization circuit,  
a logical AND combination, and  
a switched mode power supply having a normal mode  
and a low power mode, the microprocessor being coupled ~~to~~  
~~the switched mode power supply, wherein signals from the~~  
~~microprocessor for controlling the low power mode and the~~  
~~demagnetization circuit are coupled via the a~~ same output  
to the demagnetization circuit and to the switched mode  
power supply for controlling the low power mode and the  
demagnetization circuit by means of a control signal,  
wherein the control signal and an on-indicative  
signal only present in the normal mode are connected each  
to an input of said logical AND combination for coupling  
said control signal to the demagnetization circuit in  
dependency of the power on-indicative signal, and  
the control signal being in the low power mode a  
square wave signal for providing a burst mode.
2. (original) Arrangement according to claim 1, wherein the  
microprocessor comprises a single pin for controlling the  
low power mode as well as the demagnetization circuit.
3. (Cancelled)

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4. (currently amended) Arrangement according to claim 1, wherein the ~~control signal from the microprocessor and a power on indicative signal are combined via a logical AND combination is, for example via an AND gate,~~ for controlling the demagnetization circuit.
5. (currently amended) Arrangement according to claim 1, wherein the power on-indicative signal is a supply voltage being provided by the switched mode power supply only during the normal mode.
6. (currently amended) Arrangement according to claim 1, wherein the ~~control signal from the microprocessor is in the low power mode a square wave signal for providing a burst mode, the~~ a duty cycle of the square wave signal defines ~~defining~~ the switching cycles of the switched mode power supply.
7. (currently amended) Arrangement according to claim 1, wherein the control signal ~~from the microprocessor for controlling the demagnetization circuit~~ is "enable" for a time sufficient to provide a demagnetization of a picture tube, when the switched mode power supply is switched to the normal mode.
8. (currently amended) Arrangement according to claim 6, wherein the "enable" signal ~~for the demagnetization circuit~~ has a duration of 0,5 to 3 sec., and is switched to "low" after the demagnetization ~~phase~~.

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9. (currently amended) Arrangement comprising  
a microprocessor,  
a demagnetization circuit,  
a logical AND combination, and  
a switched mode power supply having a normal mode  
and a low power mode, the microprocessor being coupled to  
~~the switched mode power supply, wherein the~~  
~~microprocessor comprises one single pin for controlling~~  
~~the low power mode as well as the demagnetization~~  
~~circuit, via a same output to said demagnetization~~  
circuit and to said switched mode power supply for  
controlling said low power mode and the demagnetization  
circuit by means of a control signal,  
wherein the control signal and an on-indicative  
signal only present in said normal mode are connected  
each to an input of said logical AND combination for  
coupling the control signal to the demagnetization  
circuit in dependency of the power on-indicative signal.
10. (original) Display unit, comprising an arrangement  
according to claim 9.